Chapter 8

Refinement testing.

Summary. The theory of refinement testing. The refinement testing method.

In the previous chapter we gave the theoretical basis of a functional testing method for systems specified as X-machines. The method generates test sets from the specification that ensure that the system is fault-free provided that it is made of fault-free components and meets some “design for testing” requirements.

In practice, however, specifications of complex systems are seldom constructed from scratch. Instead, a process of refinement is used. This involves building several intermediary models \( M_1, M_2, \ldots, M_n \), where \( M_{i+1} \) is a refinement of some sort of \( M_i \) and \( M_n \) is the final system specification. Obviously, once the complete specification \( M_n \) exists, we can always test its implementation using the Stream X-Machine Testing (SXMT) method. However, for large scale systems this approach is not desirable since a single X-machine specification is difficult to handle. Also, the extremely large state space of such a machine will result in an unmanageable test set. Instead, a more convenient approach would be to construct the test set incrementally by generating test sets for all intermediary models. In doing this, one would hope that the test set of the \( i \)'th model can be reused in the generation of that of the \( (i+1) \)'th model, so that the construction of the complete test set will also be a refinement process. This chapter will address this problem in the context of the state refinement defined in chapter 6.

8.1. Theoretical basis of refinement testing.

First, let us describe clearly the problem and the approach employed. Let \( M_1 = (\text{Input}, \text{Output}, Q_1, \text{Memory}, \Phi, F_1, q_{01}, m_0) \) and \( M'_1 = (\text{Input}', \text{Output}', Q'_1, \text{Memory}', \Phi', F'_1, q_{01}', m_{0}) \) be two stream X-machine specifications of a system so that \( M'_1 \) is a state refinement of \( M_1 \) w.r.t. \((u, v)\). Then our aim is to generate an input set that tests \( M'_1 \) against its implementation.

We assume that this implementation can be modelled as a stream X-machine \( M'_2 = (\text{Input}', \text{Output}', Q'_2, \text{Memory}', \Phi', F'_2, q_{02}', m_{0}') \) with the same type \( \Phi' \) as \( M'_1 \). Furthermore, we will assume that \( M'_2 \) can be expressed as a state refinement w.r.t. \((u, v)\) of a stream X-machine \( M_2 \) and that \( M_2 \) has the same type, \( \Phi \), as \( M_1 \).

The way in which these requirements can be enforced on the system implementation will be discussed later. The arrangement is illustrated in Figure 8.1.
Our approach will be to implement and test separately the refining machines used in the construction of $M_1'$ and to test the whole system for integration using a test set that is an augmentation of the test set of $M_1$ and $M_2$.

Before we go any further let us introduce the following concept.

**Definition 8.1.1.**
Let $A$ and $A'$ be two finite alphabets so that $\text{seq}(A')$ covers $\text{seq}(A)$ w.r.t. $u$ and let $Y \subseteq \text{seq}(A)$, $Y' \subseteq \text{seq}(A')$ be two sets. Then we say that $Y'$ refines $Y$ w.r.t. $u$ if the following is true.

$$\forall a^* \in Y, \exists a'^* \in Y' \text{ with } u^*(a'^*) = a^*$$

Recall that $u^*$, (Definition 6.3.1.1), takes several applications of $u$ and concatenates them together in $A$, so that $a^{**}$ is a concatenation of a finite sequence of elements from $Y''$.

If we denote by $u^I: A \rightarrow \text{seq}(A')$ a function for which $u \cdot u^I$ is the identity and by $u^I*: \text{seq}(A) \rightarrow \text{seq}(A')$ the sequential generalisation of $u^I$, then for any $Y \subseteq \text{seq}(A)$, $Y' = u^I*(Y)$ refines $Y$.

**Example 8.1.1.**
If for $n \geq 0$, $u_n: \text{seq}(\text{CHARS} \cup \{\text{backspace}, \text{enter}\}) \rightarrow \text{WORDS}_n$ is as in example 6.3.1.2 then $Y$ refines $Y$ w.r.t. $u_2$, and $Z'$ refines $Y$ w.r.t. $u_2$ where:

$Y = \{<>, 1', 1::1', 1::0', 1::1, 1::0' 1:1 1::0' 1:11\}$ and

$Y' = \{<>, 1::enter, 1::1::enter, 1::0::enter::1::1::enter,$

$1::0::enter::1::0::enter::1::1::enter\}$

$Z' = \{<>, 1::0::backspace::enter, 1::1::0::backspace::enter,$

$1::0::0::backspace::enter::1::1::0::backspace::enter,$

$1::0::0::backspace::enter::1::0::0::backspace::enter::1::1::0::backspace::enter\}$
Let us now introduce the notation we shall be using in this section and state the assumptions we shall be making.

Let \( M_1 = (\text{Input}, \text{Output}, Q_1, \text{Memory}, \Phi, F_1, q_{01}, m_0) \) and \( M_2 = (\text{Input}, \text{Output}, Q_2, \text{Memory}, \Phi, F_2, q_{02}, m_0) \) be two stream X-machines with the same type, \( \Phi \) and initial memory, \( m_0 \). Let \( M_1' = (\text{Input}', \text{Output}', Q_1', \text{Memory}', \Phi', F_1', q_{01}', m_0') \) and \( M_2' = (\text{Input}', \text{Output}', Q_2', \text{Memory}', \Phi', F_2', q_{02}', m_0') \) be state refinements of \( M_1 \) and \( M_2 \) respectively w.r.t. \((u, v)\) that also have the same type, \( \Phi' \) and initial memory, \( m_0' \). For simplicity we will consider that the key states of \( M_1' \) and \( M_2' \) respectively are \( K_1 = Q_1 \) and \( K_2 = Q_2 \), hence \( q_{01}' = q_{01} \) and \( q_{02}' = q_{02} \). This will not affect the generality of the results proven. We denote by \( A_1, A_2, A_1' \) and \( A_2' \) the associated automata of \( M_1, M_2, M_1' \) and \( M_2' \) respectively and \( f_1, f_2, f_1', f_2' \) the functions computed by these machines.

Let \( \{M_1'(q_1) : q_1 \in Q_1\} \) be the refining set of \( M_1' \), with for any \( q_1 \in Q_1 \), \( M_1'(q_1) = (\text{Input}', \text{Output}', R_{q_1}, \text{Memory}', \Phi', F_{q_1}, q_1, m_{q_1}, T_{q_1}) \). For any \( q_1 \in Q_1 \) we denote by \( A_1'(q_1) \) the associated automaton of \( M_1'(q_1) \). Similarly \( \{M_2'(q_2) : q_2 \in Q_2\} \) is the refining set with for any \( q_2 \in Q_2 \), \( M_2'(q_2) = (\text{Input}', \text{Output}', R_{q_2}, \text{Memory}', \Phi', F_{q_2}, q_2, m_{q_2}, T_{q_2}) \) and \( A_2'(q_2) \) the associated automaton of \( M_2'(q_2) \).

Note that the type of all refining machines was considered to be \( \Phi' \). This is possible since not all processing functions have to be used as arc labels.

We shall assume that the following requirements are met.
1. \( A_1 \) is a minimal finite state machine.
2. The types \( \Phi \) and \( \Phi' \) are t-complete w.r.t. \( \text{Memory} \) and \( \text{Memory}' \) respectively and output-distinguishable.
3. For any \( q_1 \in Q_1 \), \( A_1'(q_1) \) is an accessible finite state machine.
4. All refinement machines have been tested against their corresponding implementations using the SXMT method. Thus for any \( q_2 \in Q_2 \) there exists \( q_1 \in Q_1 \) so that \( A_1'(q_1) \) and \( A_2'(q_2) \) are equivalent.

Also, for simplicity we shall assume that \( A_2 \) is accessible and for any \( q_2 \in Q_2 \) \( A_2'(q_2) \) is accessible. This will not reduce in any way the generality of our results since the accessible part of \( A_2' \) - and thus the input/output behaviour of \( M_2' \) - will not be affected by this assumption.

**Lemma 8.1.1.**
Let \( M_1, M_2, M_1', M_2' \) be four stream X-machines as described above. Then \( A_1' \) and \( A_2' \) are equivalent if there exists \( e : A_2 \to A_1 \) a surjective finite state machine morphism such that for
any \( q_2 \in Q_2 A _2 ' (q_2) \) and \( A _1 ' (e(q_2)) \) are equivalent.

**Proof:**

We define a relation \( e' : Q_2 ' \leftrightarrow Q_1 ' \) by:

\[
q_2 ' e' q_1 ' \text{ if and only if there exist } q_2 \in Q_2, q_1 \in Q_1 \text{ with } e(q_2) = q_1 \text{ and } q_2 ' \text{ and } q_1 ' \text{ are right-congruent (see Definition 7.1.7) states in } A _2 ' (q_2) \text{ and } A _1 ' (q_1) \text{ respectively.}
\]

We prove that \( e' \) satisfies the requirements of Definition 7.1.7, that is, it is a right congruence, too.

1) Obviously, \( q_02 ' e' q_01 ' \).

2) Let \( q_2 ' e' q_1 ' \) and let \( q_2 \in Q_2, q_1 \in Q_1 \) with \( e(q_2) = q_1 \) so that \( q_2 ' \) and \( q_1 ' \) are right-congruent states in \( A _2 ' (q_2) \) and \( A _1 ' (q_1) \) respectively.

Then there exists an arc \( \Phi : q_2 ' \rightarrow \theta _2 ' \) in \( A _2 ' (q_2) \) if and only if there exists an arc \( \Phi : q_1 ' \rightarrow \theta _1 ' \) in \( A _1 ' (q_1) \), where \( \theta _2 ' \) and \( \theta _1 ' \) are right-congruent states in \( A _2 ' (q_2) \) and \( A _1 ' (q_1) \) respectively. Thus there exists an arc \( \Phi : q_2 ' \rightarrow \theta _2 '' \) in \( A _2 \) if and only if there exists an arc \( \Phi : q_1 ' \rightarrow \theta _1 '' \) in \( A _1 \).

It remains to be shown that \( \theta _2 '' \sim e' \theta _1 '' \). If \( \theta _1 ' \) and \( \theta _2 ' \) are both non-terminal states of \( M _1 (q_1) \) and \( M _2 (q_2) \) then \( \theta _2 '' = \theta _2 ' \) and \( \theta _2 '' = \theta _2 ' \) hence \( \theta _2 '' \sim e' \theta _2 '' \). Otherwise let us assume that \( \theta _2 ' \) is a terminal state in \( M _2 (q_2) \) and let \( p : q_2 \rightarrow \theta _2 ' \) and \( p' : q_1 \rightarrow \theta _1 ' \) be paths in \( A _2 (q_2) \) and \( A _1 (q_1) \) respectively (such paths exist since \( \theta _2 ' \) and \( \theta _1 ' \) are right-congruent states). Let also \( m \in Memory \) be a memory value of \( M _2 \) that is attainable in \( q_2 \) (such \( m \) exists since \( A _2 \) is accessible and \( \phi \) is t-complete w.r.t. \( Memory \)) and let \( m ' \in h ^{-1} (m) \). Since \( \Phi ' \) is t-complete w.r.t. \( Memory \) there exists an input sequence \( s ^* \) with \( (m', s ^*) \in \text{domain } [p'] \). Since \( M _2 ' (q_2) \) is a refining machine it follows that \( s ^* \in \text{domain } u \) and there exists an arc \( \phi _2 : q_2 \rightarrow \theta _2 '' \) in \( A _2 \) with \( (m, u (s ^*)) \in \text{domain } \phi _1 \).

Since \( M _1 (q_1) \) is a refining machine w.r.t. \( u, s ^* \in \text{domain } u \) and \( (m', s ^*) \in \text{domain } [p'] \) it follows that \( \theta _1 ' \) is a terminal state in \( M _1 (q_1) \). Also, since \( e \) is a finite state machine morphism and \( e(q_2) = q_1 \) it is easy to see that \( m \) is also a memory value of \( M _1 \) that is attainable in \( q_1 \). Thus there exists an arc \( \phi _1 : q_1 \rightarrow \theta _1 '' \in A _1 \) with \( (m, u ^*(s ^*)) \in \text{domain } \phi _1 \). Since \( e(q_2) = q_1 \) and \( M _1 \) and \( M _2 \) are deterministic it follows that \( \phi _1 = \phi _2 \). Hence \( \theta _2 '' \sim e' \theta _1 '' \).

**Lemma 8.1.2.**

Let \( M _1, M _2, M _1 ' \), \( M _2 ' \) be as above, let \( Y \subseteq \text{seq(Input)} \) be a test set of \( M _1 \) and \( M _2 \) and \( Y ' \subseteq \text{seq(Input')} \) a set of sequences of \( Input ' \) that refines \( Y \) w.r.t. \( u \). If \( \forall s ^* \in Y, f _1 (s ^*) = f _2 (s ^*) \) then there exists a surjective finite state machine morphism, \( e : A _2 \rightarrow A _1 \).

**Proof:**

If \( \forall s ^* \in Y, f _1 (s ^*) = f _2 (s ^*) \Rightarrow \forall s ^* \in Y, v ^*(f _1 (s ^*)) = v ^*(f _2 (s ^*)) \Rightarrow \forall s ^* \in Y, f _1 (u ^*(s ^*)) = f _2 (u ^*(s ^*))) \Rightarrow \forall s ^* \in Y, f _1 (s ^*) = f _2 (s ^*). \) Hence \( A _1 \) and the minimal automaton of \( A _2 \) are isomorphic. Since \( A _1 \) is minimal and \( A _2 \) is accessible there exists a surjective morphism, \( e : A _2 \rightarrow A _1 \).
Definition 8.1.2.
Let \( M_1'(q_1): q_1 \in Q_1 \) be the refining set of \( M_1' \) as above. Then \( X_{q_1} \subseteq \text{seq}(\Phi') \) is called a distinguishing set of \( A_1'(q_1) \) if for any \( \theta_1 \in Q_1 \) either \( A_1'(q_1) \) and \( A_1'(\theta_1) \) are equivalent or \( X_{q_1} \) distinguishes between \( q_1 \) and \( \theta_1 \) as states in \( A_1'(q_1) \) and \( A_1'(\theta_1) \) respectively.

Example 8.1.2.
For the refinement of Example 6.3.3.2 we can take
\[
X_A = \{\text{type\_ch1} :: \text{wrong\_name}'\}
\]
\[
X_B = \{\text{type\_ch1} :: \text{wrong\_psw}'\}
\]
\[
X_C = \{\text{type\_ch1} :: \text{type\_ch1} :: \text{new\_psw}'\}
\]

Definition 8.1.3.
Let \( S \) be a state cover of \( A_1 \), \( k \) a positive integer and let \( t: \text{seq}(\Phi) \to \text{seq}(\text{Input}) \) be a fundamental test function of \( M_1 \). Let also
\[
P_k = \{ p \in S : (\Phi^k \cup \Phi^{k-1} \cup ... \cup \{< >\}) : \exists p \text{ a path in } A_1 \text{ that starts in } q_{01} \}
\]
For any \( p \in P_k \) let \( q_p \in Q_1 \) be the end state of \( p \) and let \( t(p) = s_p \). Since \( \Phi \) is \( t \)-complete w.r.t. Memory there exists \( m_p \in \text{Memory} \) with \( \pi_2(p(m_0, s_p \ast)) = m_p \). Let \( s_p \ast' \in \text{seq}(\text{Input}') \) with \( u\ast(s_p \ast') = s_p \ast \). We will denote \( s_p \ast' \) by \( u^{-1}(t(p)) \).

Since \( M_1' \) is a state refinement of \( M_1 \) there exists \( m_p' \in h^{-1}(m_p) \) and a path \( p' \) in \( A_1 \) from \( q_01 \) to \( q_p \) with \( \pi_2(p'(m_0', s_p \ast')) = m_p' \). Then let \( t_p = t_{q_p, m_p} ' \) be a test function of \( M_1'(q_p) \) w.r.t. \( (q_p, m_p') \).

Also let \( X_p \subseteq \text{seq}(\Phi') \) be a distinguishing set of \( A_1'(q_p) \) as defined above.

Then \( U_k \), a set of sequences of Input', defined by:
\[
U_k = \bigcup_{p \in P_k} \{ u\ast'(t(p)) \} \bullet (t_p(X_p))
\]
will be called a \( k \)-distinguishing set of the refining set.

Example 8.1.3.
Let \( M \) and \( M', \) be the machines from Example 6.3.3.2. Then a state cover of \( M \) is:
\[
S = \{< >, \text{good\_name}, \text{good\_name} :: \text{good\_psw}\}
\]
For \( k = 2 \), \( P_2 = \{p_1, ..., p_{12}\} \), where
\[ p_1 = \langle \cdot \rangle,\ p_j: A \rightarrow A \]
\[ p_2 = \text{good\_name},\ p_2: A \rightarrow B \]
\[ p_3 = \text{wrong\_name},\ p_3: A \rightarrow A \]
\[ p_4 = \text{good\_name} :: \text{good\_psw},\ p_4: A \rightarrow C \]
\[ p_5 = \text{good\_name} :: \text{wrong\_psw}, p_5: A \rightarrow A \]
\[ p_6 = \text{wrong\_name} :: \text{good\_name},\ p_6: A \rightarrow B \]
\[ p_7 = \text{wrong\_name} :: \text{wrong\_name},\ p_7: A \rightarrow A \]
\[ p_8 = \text{good\_name} :: \text{good\_psw} :: \text{new\_psw}, p_8: A \rightarrow A \]
\[ p_9 = \text{good\_name} :: \text{wrong\_psw} :: \text{wrong\_name}, p_9: A \rightarrow B \]
\[ p_{10} = \text{good\_name} :: \text{wrong\_psw} :: \text{wrong\_name}, p_{10}: A \rightarrow A \]
\[ p_{11} = \text{good\_name} :: \text{good\_psw} :: \text{new\_psw} :: \text{good\_name}, p_{11}: A \rightarrow B \]
\[ p_{12} = \text{good\_name} :: \text{good\_psw} :: \text{new\_psw} :: \text{wrong\_name}, p_{12}: A \rightarrow A \]

We will assume that ‘01’ is a valid username and ‘1’ is not a valid username, i.e. \( \text{map}(\text{‘01’}) = \text{‘11’} \).

Then we can take \( s_{i^*}, s_{i^*}', m_i \) and \( m_i' \), \( i = 1...12 \), as follows:

\[
\begin{align*}
 s_{1^*} &= \langle \cdot \rangle \\
 s_{2^*} &= \text{‘01’} \\
 s_{3^*} &= \text{‘1’} \\
 s_{4^*} &= \text{‘01’} \ ‘11’ \\
 s_{5^*} &= \text{‘01’} \ ‘0’ \\
 s_{6^*} &= \text{‘1’} \ ‘01’ \\
 s_{7^*} &= \text{‘1’} \ ‘1’ \\
 s_{8^*} &= \text{‘01’} \ ‘11’ \ ‘00’ \\
 s_{9^*} &= \text{‘01’} \ ‘0’ \ ‘01’ \\
 s_{10^*} &= \text{‘01’} \ ‘0’ \ ‘1’ \\
 s_{11^*} &= \text{‘01’} \ ‘11’ \ ‘00’ \ ‘01’ \\
 s_{12^*} &= \text{‘01’} \ ‘11’ \ ‘00’ \ ‘1’
\end{align*}
\]

\[
\begin{align*}
 s_{1^*}' &= \langle \cdot \rangle \\
 s_{2^*}' &= 0 :: 1 :: \text{enter} \\
 s_{3^*}' &= 1 :: \text{enter} \\
 s_{4^*}' &= 0 :: 1 :: \text{enter} :: 1 :: 1 :: \text{enter} \\
 s_{5^*}' &= 0 :: 1 :: \text{enter} :: 0 :: \text{enter} \\
 s_{6^*}' &= 1 :: \text{enter} :: 0 :: 1 :: \text{enter} \\
 s_{7^*}' &= 1 :: \text{enter} :: 1 :: \text{enter} \\
 s_{8^*}' &= 0 :: 1 :: \text{enter} :: 1 :: 1 :: \text{enter} :: 0 :: 0 :: \text{enter} \\
 s_{9^*}' &= 0 :: 1 :: \text{enter} :: 0 :: \text{enter} :: 0 :: 1 :: \text{enter} \\
 s_{10^*}' &= 0 :: 1 :: \text{enter} :: 0 :: \text{enter} :: 1 :: \text{enter} \\
 s_{11^*}' &= 0 :: 1 :: \text{enter} :: 1 :: 1 :: \text{enter} :: 0 :: 0 :: \text{enter} :: 0 :: 1 :: \text{enter} \\
 s_{12^*}' &= 0 :: 1 :: \text{enter} :: 1 :: 1 :: \text{enter} :: 0 :: 0 :: \text{enter} :: 1 :: \text{enter}
\end{align*}
\]
If $X_A$, $X_B$ and $X_C$ are those from Example 8.1.2 then we can take:

$$U_2 = \{ s_1^{*\prime}, t_A^{*\prime}, s_2^{*\prime}, t_B^{*\prime}, s_3^{*\prime}, t_A^{*\prime}, s_4^{*\prime}, t_A^{*\prime}, t_B^{*\prime}, s_5^{*\prime}, t_A^{*\prime}, s_6^{*\prime}, t_A^{*\prime}, s_7^{*\prime}, t_A^{*\prime}, s_8^{*\prime}, t_A^{*\prime}, s_9^{*\prime}, t_B^{*\prime}, s_{10}^{*\prime}, t_A^{*\prime}, s_{11}^{*\prime}, t_B^{*\prime}, s_{12}^{*\prime}, t_A^{*\prime} \}$$

where $t_A^{*\prime} = 1 :: \text{enter}$, $t_B^{*\prime} = 0 :: \text{enter}$, $t_C^{*\prime} = 0 :: 0 :: \text{enter}$

Theorem 8.1.1.
Let $M_1, M_2, M_1', M_2'$ be as in Definition 8.1.3 above. Let $Y \subseteq \text{seq}(\text{Input})$ be a test set of $M_1$ and $M_2$, $Y' \subseteq \text{seq}(\text{Input})$ a set that refines $Y$ w.r.t. $u$ and $U_k$ a $k$-distinguishing set of the refining set of $M_1'$, where $k = \text{Card}(Q_2) - \text{Card}(Q_1)$ is the difference between the number of states of $M_2$ and the number of states of $M_1$ and let $Y'' = Y' \cup U_k$. If $\forall s^{*\prime} \in Y'', f_1'(s^{*\prime}) = f_2'(s^{*\prime})$ then $A_1'$ and $A_2'$ are equivalent.

Proof:
From the lemma it follows that there exists $e: A_2 \to A_1$ a surjective finite state machine morphism. Then the set $P_k$ defined as in Definition 8.1.3 is a state cover of $A_2$. Indeed since $e: A_2 \to A_1$ is a finite state morphism at least $\text{Card}(Q_1)$ states of $A_2$ will be accessed by a path in $S$. Also $\forall i = 1, \ldots, (k-1)$ the set $P_{i+1}$ will access at least one of the states that has not been accessed by $P_i$.

Thus for any $q_2 \in Q_2$ there exists $p \in P_k$ so that $p: q_02 \to q_2$ is a path in $M_2$ and $p: q_01 \to e(q_2)$ is a path in $M_1$. If $s_{p^{*\prime}}, m_p$ and $m_p'$ are those from Definition 8.1.3 then there exists $p': q_01 \to e(q_2)$ a path in $M_1'$ with $\pi_2(p'(m_0', s_{p'^{*\prime}})) = m_p'$. Since $M_2'$ is a state refinement of $M_2$ there exists $p'': q_02 \to q_2$ a path in $M_2'$ and $m_p'' \in h^{-1}(m_p)$ with $\pi_2(p''(m_0', s_{p'^{*\prime}})) = m_p''$. From the output-distinguishability of $\Phi'$ it follows that $p'' = p'$ and $m_p'' = m_p'$.

Since $\Phi'$ is output-distinguishable the application of $t_{q_p}(X_p)$ in $q_2$ and $e(q_2)$ respectively with initial memory $m_p'$ will ensure that $A_2(q_2)$ and $A_1'(q_1)$ are equivalent. From Lemma 8.1.1. it follows that $A_1'$ and $A_2'$ are equivalent.
8.2. The refinement testing method.

The previous theorem can be used as the theoretical basis of a method for testing stream X-machine specifications constructed through a process of state refinement. The method will use a two phase approach in which the refining machines are implemented and tested first and then the entire system is tested for integration.

**Phase 1. Testing the refining machines.**

These will be implemented as separate subprograms or pieces of code that can be tested in isolation from the rest of the system. Each such *subimplementation* will communicate with the rest of the system through one or more “transfer variables” whose values will correspond to the terminal states of the refining machine. Thus, the testing procedure will not only have to ensure that the input/output behaviour of the refining machine coincides with that of its implementation but also that each transfer variable identifies correctly its corresponding terminal state.

Therefore we need to adapt our refining machines so that they possess the transfer variable linking mechanism. This implies that any component technology must also incorporate a suitable mechanism for specifying transfer variables. The process can be described formally by an augmented refining machine as defined next.

**Definition 8.2.1.**

Let \( M'(q_1) = (\text{Input}', \text{Output}', R_{q_1}, \text{Memory}', \Phi', F_{q_1}, q_1, m_{q_1}', T_{q_1}) \) be the refining machine in \( q_1 \), \( T_{q_1} = \{t_1, ..., t_m\} \) the set of terminal states and let \( \text{Input} \neq \text{Input}' \) and \( \text{Output}_{T_{q_1}} = \{\text{out}_1, ..., \text{out}_m\} \) a set with \( \text{Output}_{T_{q_1}} \cap \text{Output}' = \emptyset \). Then a stream X-machine defined by:

\[
M_{A}(q_1) = (\text{Input}_A, \text{Output}_A, R_{q_1}, \text{Memory}', \Phi'_A, F_{q_1A}, q_1, m_{q_1}', T_{q_1})
\]

where:

1. \( \text{Input}_A = \text{Input}' \cup \{\text{in}_{q_1}\} \).
2. \( \text{Output}_A = \text{Output}' \cup \text{Output}_{T_{q_1}} \).
3. \( \Phi'_A = \Phi' \cup \Psi_{q_1}' \), with \( \Psi_{q_1}' = \{\psi_1', ..., \psi_m'\} \) such that, for any \( t \in T_{q_1}, \psi_t' \) is defined by

\[
\psi_t'(m, \text{in}_{q_1}) = (\text{out}_t, m, m \in \text{Memory})
\]
4. \( F_{q_1A}: R_{q_1} \times \Phi'_A \rightarrow R_{q_1} \) is defined by

\[
F_{q_1A}(q', \phi') = \begin{cases} 
q', & \text{if } \exists j = 1...m \text{ with } q' = t_j \text{ and } \phi' = \psi_{q_1}' \\
\perp, & \text{otherwise}
\end{cases}
\]

is called the **augmented refining machine in** \( q_1 \).

In other words each terminal state \( t \) will be identified in the augmented machine by an extra output symbol \( \text{out}_t \) produced by an extra processing function \( \psi_t \) that labels a loop from \( t \). These loops will be triggered by an extra input \( \text{in}_{q_1} \). The extra outputs will correspond to the values of transfer variables used by the implementation. The extra input \( \text{in}_{q_1} \) does not necessarily exist in the implementation; it will correspond to a means of identifying the value of the transfer variables for each terminal state. The augmented refining machines of Example 6.3.5 may be found in figure 8.2.

If an augmented machine \( M_{A}(q_1) \) is tested against its corresponding implementation using
the SXMT method and the implementation passes the required tests then this can be modelled as a stream X-machine $M_2'(q_2)$ whose associated automaton is equivalent to that of $M_1'(q_1)$ and whose terminal states - that is the transfer variables of the implementation - will match the terminal states of $M_1'(q_1)$.

Obviously, the SXMT method will require that the basic processing functions $\Phi'$ satisfy the “design for testing conditions” and are implemented correctly.

**Phase 2. Testing the system for integration**

The system will be composed of the above implementations that communicate through the transfer variables. If the first phase of our testing method has been completed then it easy to see that the entire system can be modelled by a stream X-machine $M_2'$ with type $\Phi'$ that is a state refinement of a machine with the same type $\Phi$ as $M_1$. Thus Theorem 8.1.1. can be used to generate a test set $Y' = Y \cup U_k$ whose application guarantees that the system is functionally equivalent to its specification. Recall that if $W$ is a characterisation set of $A_1$ then

$$Y = t (S \bullet (\Phi^{k+1} \cup \Phi^k \cup ... \cup \Phi) \bullet W)$$

is a test set of $M_1$ and $M_2$, thus $Y' = u^{-1}*Y$.

**Complexity and upper bounds of the test set.**

If the complexity of $u^{-1}$is $C(u^{-1})$, the complexity of each $\phi \in \Phi$ is at most $C$ and the complexity of each procedure used to determine a distinguishing set of a refining machine is at most $C_D$ then the complexity of the algorithm that generates the test set $Y'$ is proportional to

$$C(u^{-1}) \cdot C \cdot p \cdot r^{k+1} \cdot n^2 \cdot (n+2k) + r^k \cdot n \cdot C_D$$

where $n = \text{Card}(Q_1)$, $k = \text{Card}(Q_2) - \text{Card}(Q_1)$, $p = \text{Card}(\text{Input})$ and $r = \text{card}(\Phi)$.

If all values of $u^{-1}$are sequences of at most $l$ elements and the length of any sequence used to distinguish between any two refinement modules is at most $l$, then the upper bounds for the number of test sequences required and the total length of the test set are approximately $l \cdot n^2 \cdot r^{k+1}$ and $l \cdot (n+k) \cdot r^{k+1}$ respectively.
Figure 8.1.
Design for testing requirements
As previously discussed, the “design for test conditions” (i.e. the t-completeness and output-distinguishability of the types \( \Phi \) and \( \Phi' \)) can be introduced into the specification through an augmentation of the input and/or output alphabets. However, in this case, particular care is needed to ensure that the augmented machines will still be in a state refinement relation. For example, it is easy to see that \( \Phi \) and \( \Phi' \) of Example 6.3.3.2 are output-distinguishable but not t-complete. However, the t-completeness of \( \Phi \) and \( \Phi' \) can be achieved through an extension of their input alphabets:

\[
\begin{align*}
\text{In}_e &= \text{In} \cup \{ \text{in}_1, \text{in}_2 \}, \quad \text{where } \text{In} \cap \{ \text{in}_1, \text{in}_2 \} = \emptyset \\
\text{In}'_e &= \text{In}' \cup \{ \text{in}'_1, \text{in}'_2 \}, \quad \text{where } \text{In}' \cap \{ \text{in}'_1, \text{in}'_2 \} = \emptyset
\end{align*}
\]

These extra inputs will be used to extend the processing functions in \( \Phi \) and \( \Phi' \) as follows. We only show the effect of the extra inputs, the processing functions remain unchanged elsewhere. Let \( \text{map} \in \text{MAPS}, \text{name}, \text{str} \in \text{WORDS}_2 \).

\[
\begin{align*}
\text{good\_name}_a((\text{map}, \text{name}), \text{in}_1) &= ((\text{msg}1, \text{str}), (\text{map}, \text{name})) \\
\text{wrong\_name}_a((\text{map}, \text{name}), \text{in}_2) &= ((\text{msg}2, \text{str}), (\text{map}, \text{name})) \\
\text{good\_psw}_a((\text{map}, \text{name}), \text{in}_1) &= ((\text{msg}3, \text{str}), (\text{map}, \text{name})) \\
\text{wrong\_psw}_a((\text{map}, \text{name}), \text{in}_2) &= ((\text{msg}4, \text{str}), (\text{map}, \text{name})) \\
\text{good\_name}'_a((\text{map}, \text{name}), \text{str}, \text{in}_1') &= (\text{msg}1, ((\text{map}, \text{name}), < >)) \\
\text{wrong\_name}'_a((\text{map}, \text{name}), \text{str}, \text{in}_2') &= (\text{msg}2, ((\text{map}, \text{name}), < >)) \\
\text{good\_psw}'_a((\text{map}, \text{name}), \text{str}, \text{in}_1') &= (\text{msg}3, ((\text{map}, \text{name}), < >)) \\
\text{wrong\_psw}'_a((\text{map}, \text{name}), \text{str}, \text{in}_2') &= (\text{msg}4, ((\text{map}, \text{name}), < >))
\end{align*}
\]

It is easy to see that the augmented types are both t-complete and output-distinguishable. The only thing that remains to be done is to extend the definition of the input covering (we will call this \( u_{2e} \)) alphabet by:

\[
\begin{align*}
u_{2e}(a^* :: \text{in}_1') &= \text{in}_1 \\
u_{2e}(a^* :: \text{in}_2') &= \text{in}_2 \\
\forall a^* \in \text{seq}(\text{CHARS} \cup \{ \text{backspace} \}) \text{ with } a^* :: \text{enter} \in \text{domain } u_2.
\end{align*}
\]